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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Complete if Known		
				Application Number	10/666,397	
Sheet		2	of	2	Filing Date	September 18, 2003
					First Named Inventor	Richard Nicholson
					Art Unit	2878
					Examiner Name	
					Attorney Docket Number	55123P275

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	†
JZ		DICKSON, JOHN F., "On-Chip High-Voltage Generation in MNOS Integrated Circuits Using an Improved Voltage Multiplier Technique", IEEE Journal of Solid-State Circuits, June 1976, Vol. SC-11, No. 3, Pgs. 374-378	

Examiner Signature	Jeffrey Zweizig	Date Considered	9/15/04
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

†Applicant's unique citation designation number. *Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Sokoloff, Taylor & Zafman (wtr) 08/11/2003.
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